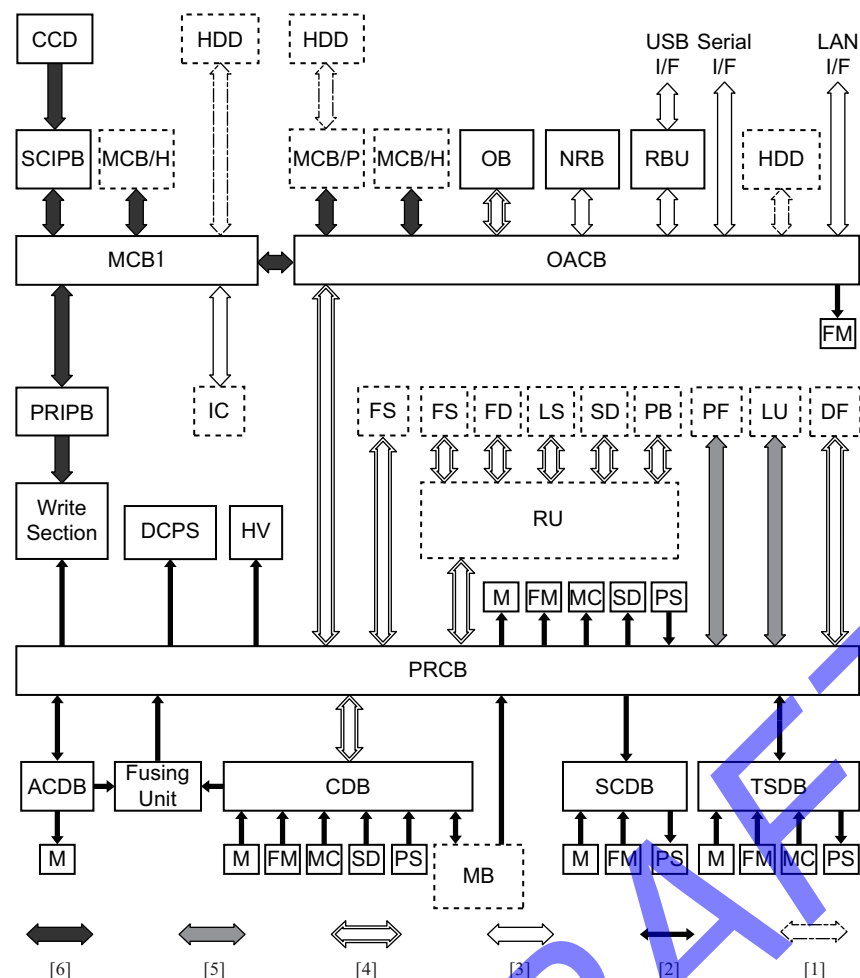


## 4. CONTROL BLOCK DIAGRAM



[1]	SATA	[2]	Individual signal line
[3]	Other bus	[4]	UART bus
[5]	Clock-synchronized serial bus	[6]	Image bus

Board name	Purpose of board	ISW target
Overall control board (OACB)	Overall condition control	Target
Memory control board /1 (MCB1)	Image memory (DRAM+HDD option) control	Non-target
Printer image processing board (PRIPB)	Image processing control for writing	Non-target
Printer control board (PRCB)	Load control of such as motor, fan, clutch, solenoid and sensor	Target
Scanner image processing board (SCIPB)	Scanner image processing board	Non-target
Memory control board /H (MCB/H)	I/F control of HDD option (option)	Non-target
Memory control board /P (MCB/P)	Image memory for thumbnail (DRAM+HDD option) control (option)	Non-target

## 5. IMAGE CREATION PROCESS

### 5.1 Image creation flow and functions

Step	Process	Functions
Step 1	Charging process	Forms a charge layer on the photo conductor drum.
Step 2	Laser exposure process	Forms an electrostatic latent image on the photo conductor drum.
Step 3	Developing process	Makes the electrostatic latent image to the visible image.
Step 4	Intermediate transfer process	Forms an image by compositing the monochromatic (YMCK) visible image on each photo conductor drum on the transfer belt.
Step 5	2nd transfer process	Transfers the image on the transfer belt to paper.
Step 6	Separation process	Separates paper after the toner transfer from the transfer belt.
Sub step 1	Drum cleaning	Removes the toner on the photo conductor drum after the intermediate transfer.